

	Search Text
1	extract\$5 with clock and logic adj cone and @AD<"20021202"
2	extract\$5 with clock and logic adj cone and @AD<"20021202" and logic\$4 adj simulat\$4 and (compress\$4 reduc\$5 compact\$4) near4 (logic\$4)
3	logic adj cone and "716"/\$.ccls.
4	logic adj cone and "716"/\$.ccls. and extract\$4 with clock adj domain
5	logic adj cone and "716"/\$.ccls. and extract\$4 with clock adj domain and logic\$4 with simulat\$4
6	logic adj cone and "716"/\$.ccls. and extract\$4 with clock adj domain and logic\$4 with simulat\$4 and (delay with (wire path line trace segment route connect\$6 interconnect\$4))
7	logic adj cone and "716"/\$.ccls. and extract\$4 with clock and (logic\$4 verif\$7) with simulat\$4 and (delay with (wire path line trace segment route connect\$6 interconnect\$4))
8	logic adj cone and "716"/\$.ccls. and extract\$4 with clock and (logic\$4 verif\$7) with simulat\$4 and (delay with (wire path line trace segment route connect\$6 interconnect\$4)) and simulat\$5 with (delay timing)
9	(logic\$4 with cone and verif\$6 and simulat\$4 and clock).clm.
10	(logic\$4 with cone and (netlist net adj list) and clock).clm.
11	logic\$4 adj cone same fan\$7
12	logic\$4 adj cone same fan\$7 and "716"/\$.ccls.
13	(logic\$4 with cone and (netlist net adj list) and clock adj domain).clm.
14	(logic\$4 with cone and (netlist net adj list) and clock and simulat\$4).clm.
15	(extract\$4 same clock same (netlist net adj list) and logic\$4 with cone and simulat\$4).clm.
16	(extract\$4 same clock same (netlist net adj list)) and logic\$4 with cone and simulat\$4 and clock adj domain
17	(extract\$ same (netlist net adj list)) and logic\$4 adj cone and simulat\$4 and clock adj domain
18	(netlist net adj list) and logic\$4 adj cone and verif\$5 with simulat\$4 and clock adj domain
19	(logic\$4 with cone and (netlist net adj list)) and clock and simulat\$4 same (delay timing)
20	cone and (netlist net adj list) and clock adj domain and simulat\$4 same (verif\$7 timing delay)
21	(logic\$4 and cone and simulat\$4 and clock and synchron\$6).clm.